```
S2LPRadioSetXtalFrequency(50000000);
/* S2LP Radio config */
S2LPRadioInit(&xRadioInit);
/* S2LP Packet config */
S2LPPktBasicInit(&xBasicInit);
S2LPRadioSetChannelSpace(CH_SPACE);
/* S2LP Radio channel setting */
S2LPRadioSetChannel(S2LP_CH_7);
S2LPSetRxSourceReferenceAddress(MY_ADDRESS);
S2LPPktBasicAddressesInit(&xBasicAddressesInit);
S2LPGpioIrqDeInit(&xIrqStatus);
S2LPGpioIrqConfig(RX_DATA_DISC,S_ENABLE);
S2LPGpioIrqConfig(RX_DATA_READY, S_ENABLE);
/* payload length config */
S2LPPktBasicSetPayloadLength(PACKAGE_LENGTH);
/* IRQ registers blanking */
S2LPGpioIrqClearStatus();
if (xIrqStatus.IRQ_RX_DATA_READY) {
        S2LPGpioIrqClearStatus();
        /* Get the RX FIFO size */
        rxPcktLen = S2LPFifoReadNumberBytesRxFifo();
        S2LPSpiReadFifo((uint8_t)rxPcktLen, vectcRxBuff);
        S2LPCmdStrobeFlushRxFifo();
        S2LPCmdStrobeRx();
   } else if (xIrqStatus.IRQ_RX_DATA_DISC) {
        S2LPGpioIrqClearStatus();
        S2LPCmdStrobeFlushRxFifo();
        S2LPCmdStrobeRx();
   }
```

Below the configuration and defines used:

```
#define BASE_FREQUENCY          868000000
```

```
#define MODULATION_SELECT          MOD_2GFSK_BT05
#define DATARATE                   19200
#define FREQ_DEVIATION             20000
#define BANDWIDTH                  100000
/*  Packet configuration parameters  */
#define PREAMBLE_LENGTH            PREAMBLE_BYTE(8)
#define SYNC_LENGTH                SYNC_BYTE(2)
#define SYNC_WORD                  0xD391
#define VARIABLE_LENGTH            S_DISABLE
#define EXTENDED_LENGTH_FIELD      S_DISABLE
#define CRC_MODE                   PKT_NO_CRC//PKT_CRC_MODE_16BITS_1
#define EN_ADDRESS                 S_ENABLE
#define EN_FEC                     S_ENABLE
#define EN_WHITENING               S_ENABLE
/*  Basic address  */
#define FILTER_ON_MY_ADDRESS                S_DISABLE//S_ENABLE
#define MY_ADDRESS                                  1
#define FILTER_ON_MULTICAST_ADDRESS         S_DISABLE
#define MULTICAST_ADDRESS                   1
#define FILTER_ON_BROADCAST_ADDRESS         S_DISABLE
#define BROADCAST_ADDRESS                   1

#define CH_SPACE                                    42725
#define EN_SWAP_PREAMBLE                    S_DISABLE
#define PACKAGE_LENGTH                              52

#define PREAMBLE_BYTE(v)                (4*v)
#define SYNC_BYTE(v)               (8*v)/**
* @brief GPIO structure fitting
*/
SGpioInit xGpio2IRQ={
  S2LP_GPIO_2,
```

```
  S2LP_GPIO_MODE_DIGITAL_OUTPUT_LP,
  S2LP_GPIO_DIG_OUT_IRQ
};
SGpioInit xGpio1IRQ={
  S2LP_GPIO_1,
  S2LP_GPIO_MODE_DIGITAL_OUTPUT_LP,
  S2LP_GPIO_DIG_OUT_IRQ
};
```